



PTO/SB/08B (07-05)

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>		Application Number	10/020,968
		Filing Date	12/19/2001
		First Named Inventor	Steven G. Schmidt
		Art Unit	2616
		Examiner Name	Habte Mered
Sheet	1	of	1
		Attorney Docket Number	2914

#### NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS); title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
HM		GREGORY L. FRAZIER & YUVAL TAMIR, The Design and Implementation of a Multi-Queue Buffer for VLSI Communication Switches, 1989 IEEE	
HM		KOHEI SHIOMOTO, MASAYUKI MURATA, YUJI OIE & MIDEO MIYAHARA, Performance Evaluation of Cell Bypass Queuing Discipline for Buffered Banyan Type ATM Switches, 1990 IEEE	

Examiner Signature	/Habte Mered/	Date Considered	12/08/2006
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/Habte Mered/



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## **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

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Sheet 1

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U. S. PATENT DOCUMENTS

## **FOREIGN PATENT DOCUMENTS**

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LIST OF ART CITED BY APPLICANT  
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87264.2560	10/020,968
APPLICANT	
SCHMIDT	

FILING DATE GROUP  
12/19/01 2661- 2662

U. S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A NM	6,078,959	06/20/00	Wright et al.	709	227	01/29/98
B						
C						
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*Mark Mendel* 08/03/05